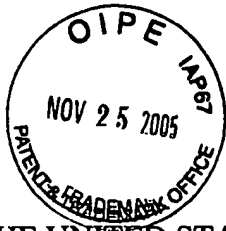


1111.68332



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Takizawa et al.
Serial No.: 10/660,053
Conf. No.: 7258
Filed: September 11, 2003
For: THIN FILM TRANSISTOR
MATRIX DEVICE AND
METHOD FOR
FABRICATING THE SAME
Art Unit: 2823
Examiner: Nguyen, Khiem D.

I hereby certify that this paper is being deposited with the United States Postal Service as FIRST-CLASS mail in an envelope addressed to: Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this date.

November 22, 2005

Date

Registration No. 37,538

Attorney for Applicant(s)

RESPONSE C

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Final Office Action dated August 22, 2005, please consider the following Remarks:

OKAY TO ENTER
K.N.
12 / 09 / 05